

In the Specification:

Please replace the paragraph starting on page 5 line 10 with the following two paragraphs (one amended, one new):

FIG. 5 is a top view of the motherboard of FIG. 1A, modified according to the present invention, and with a DiskOnKey™ flash memory connected to the inward-facing USB port[[.]];

FIG. 6 is a cross-section, through line A-A, of the motherboard of FIG. 5.

Please replace the paragraph starting on page 6 line 8 with the following amended paragraph:

A system designer of a host device based on motherboard **80** is free to include in his or her design an internal USB peripheral device connected to USB port **74** while leaving USB port **72** free for a user of the host device to connect therewith an external USB peripheral device to the host device. For example, a DiskOnKey™ flash memory could be connected to USB port **74** to serve as one of the non-volatile memories of the host device. Figure 5 shows motherboard **80** with a DiskOnKey™ flash memory **90** connected to USB port **74**. Figure 6 shows a cross section of motherboard **80** of Figure 5 taken at line A-A. Figure 6 shows that DiskOnKey™ flash memory **90** is connected only to USB port **74** and not to North Bridge controller

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